

CHIEF PATRONS

Prof. Shyam Lal Soni, Director, NITUK

Prof. Shailendra Jain, Director, SLIET

PATRONS

Dr. H. Muthusamy, Dean FW, NITUK

Dr. T. S. Arora, HoD, ECE, NITUK

CONVENERS

Prof. J. S. Ubhi, SLIET

Dr. Pankaj Kumar Pal, NITUK

Er. Pankaj Kumar Das, SLIET

COORDINATORS

Dr. Siva Kr .Tadepalli Dr. Tushar Goel

Mr. Hemant K. Singhal Ms. Sarita Yadav

TENTATIVE LIST OF SPEAKERS

Prof. Sudeb Dasgupta, IIT Roorkee

Shri H. S. Jatana, SCL Mohali

Dr. S. K. Gupta, Ministry of Electronics & IT

Dr. Vishvendra Singh Poonia, IIT Roorkee

Dr. M. S. Khatri, NIT Uttarakhand

Dr. A. P. S. Pharwaha, SLIET, Longowal

CONTACT PERSON

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ABOUT NIT UTTARAKHAND

National Institute of Technology Uttarakhand is located in the hilly terrain of Srinagar, Pauri Garhwal, Uttarakhand. NIT Uttarakhand was established in 2009 under the Act of Parliament of India by the Ministry of Human Resource Development and designated with the status of "Institute of National Importance". Currently, NIT Uttarakhand is functional from two campuses i.e. Parent campus at Srinagar, Pauri Garhwal and Satellite campus at MNIT Jaipur.

DEPARTMENT OF ELECTRONICS ENGINEERING

Department of Electronics Engineering (EC) was established in the year 2010. The department offers Undergraduate (UG), Postgraduate (PG) and Ph.D. degree programs that provide students with the knowledge and tools they need to succeed in the Electronics and Communication Engineering. Research in the department focuses on various high-impact disciplines: Signal and Image Processing, Microwave antennas, Optical communication and VLSI design and technologies. Our faculty brings state-of-the-art research, development, and design experience into the classroom, ensuring that our students and alumni are able to apply for registration as professional engineers in all part of global engineering and the scientific community.

A

Short Term Course

on

Nano-electronics & VLSI: Devices, Circuits & Systems

(November 4th - 8th, 2019)



Sponsored by

TEQIP-III, NIT Uttarakhand
under the twinning-activities with
SLIET Longowal, Punjab



Organized by

Department of Electronics Engineering
NIT Uttarakhand, Srinagar Garhwal,
Uttarakhand - 246174

INTRODUCTION

This short term course is designed to provide an overview on Nanoelectronics & VLSI device, circuits and system design. Eminent speakers from top notch Academia and Industries are invited for the same. The course shall be offered from 4th-8th November 2019, in the Department of Electronics Engineering, NIT Uttarakhand, Srinagar (Garhwal). It is sponsored by the TEQIP III under Twinning Activities with SLIET Longowal, Punjab.

COURSE OBJECTIVES

Semiconductor devices are the basic building blocks of integrated chips (ICs). Due to the rapid development of semiconductor technology, the dimensions of the electronic devices are shrinking and the density of transistors have increased in the ICs which enables multiple features and functionalities in the electronic gadgets where energy, power, speed and area are the important design metrics. The proportionate change in the device dimensions in the nanometer regime also faces several challenges, worldwide. The nanoscale devices need quantum mechanics in order to model and understand the device behavior. Hence, this STC will focus on various aspects of Nano-electronic devices and the possibility of circuits and system design.

PROSPECTIVE PARTICIPANTS

This STC is open for participants from Industries, PSUs and Academic Institutions Faculties, Ph.D. Research Scholars and Undergraduate, Post-graduate students. The number of participants for the course are limited to thirty (30) on first come first serve basis.

REGISTRATION DETAILS

- ❖ A course registration fee of ₹ 1,000/- will be charged from participants and an online payment have to be made.
- ❖ There is **NO registration-fee for participants from TEQIP III institutions.**
- ❖ No TA/DA will be paid to any participant.
- ❖ Tea-Snacks and Lunch will be served during the STC.
- ❖ Accommodation on sharing basis may be provided to out-station participants only, upon prior request.
- ❖ Spot registration facility is also available.

For online-payment, the bank account details are as follows:-

A/C Name:- National Institute of Technology
Uttarakhand

Bank Name:- SBI, Srinagar-Garhwal

Account No:- 37530566069

IFSC Code:- SBIN0003181

*Filled registration form along-with proof of online transaction must be e-mailed to:-
pankajpal86@nituk.ac.in*

REGISTRATION FORM

A Short Term Course
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Nano-electronics & VLSI: Devices, Circuits & Systems

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Sponsored by

TEQIP III, NIT Uttarakhand

1. Full Name
2. Designation
3. Organization
-
4. Academic Qualification
5. Age
6. Gender (Male/Female/Others).....
7. Mailing address
-
-
8. Email
9. Phone/Mobile

Place :

Signature of applicant

Authorized Signature with seal